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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/769,417	01/26/2001	Yoshio Fudeyasu	49657-962	4245

7590

07/08/2002

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EXAMINER

NAMAZI, MEHDI

ART UNIT

PAPER NUMBER

2187

DATE MAILED: 07/08/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/769,417

Applicant(s)

Fudeyasu

Examiner

Mehdi Namazi

Art Unit

2187

— The MAILING DATE of this communication appears on the cover sheet with the correspondence address —

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE three MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on Jan 26, 2001
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 35 C.D. 11; 453 O.G. 213.
- ## Disposition of Claims
- 4) ☒ Claim(s) 1-15 is/are pending in the application
- 4a) Of the above, claim(s) _____ is/are withdrawn from consideration
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-3, 9, and 11-15 is/are rejected.
- 7) ☒ Claim(s) 4-8 and 10 is/are objected to.
- 8) ☐ Claims _____ are subject to restriction and/or election requirements

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☒ All b) ☐ Some* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- *See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____ 6) ☐ Other: _____

Art Unit: 2187

DETAILED ACTION

Claims 1-15 are presented for examination. This office action is in response to the application filed on January 26, 2001.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-3, 9 and 11-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Chappell et al. (Chappell) (U.S. Patent No. 5,204,841).

As per claims 1 and 9, Chappell teaches a semiconductor memory device, comprising:

a plurality of input terminals for receiving write data, a control signal and an address signal (fig. 1); and at least one output terminal for outputting read data, read data being allowed to be different in bit width from said write data (col. 9, lines 27-34).

Art Unit: 2187

As per claims 2 and 11, Chappell teaches further comprising a data control circuit for changing a number of terminal(s) functioning as the input terminal(s) and a number of terminal(s) functioning as the output terminal(s) (col. 8, lines 8-18) .

As per claim 3, Chappell teaches wherein the input terminals are coupled to a first bus, and the at least one output terminal is coupled to a second bus, and each of the first and second buses is a unidirectional bus for transferring a signal or data in one direction(fig. 5) .

As per claim 12, Chappell teaches wherein the memory includes circuitry for simultaneously inputting and outputting the write data and the read data(col. 1, lines 26-32) .

As per claim 13, Chappell teaches wherein memory controller includes circuitry for simultaneously transferring write data and read data(fig. 5) .

As per claim 14, Chappell teaches wherein first unidirectional bus transfers the write data, the control signal and the address signal through common data bus lines(col. 4, lines 19-30) .

As per claim 15, Chappell teaches wherein the input terminals receive the write data, the control signal and the address signal at common terminals(fig. 1) .

Art Unit: 2187

Allowable Subject Matter

3. Claims 4-8, 10 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

US Patent No. 5,001,671 (Koo et al.) Teaches Controller for dual ported memory.

US Patent No. 5,003,475 (Kerber et al.) Teaches Medical image system including means to increase Data transfer.

US Patent No. 5,007,022 (Leigh) Teaches two-port two-transistor DRAM.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mehdi Namazi whose telephone number is (703) 306-2758. The examiner can normally be reached on Monday-Thursday from 7:00 to 5:30. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Do Hyun Yoo, can be reached on (703)

Art Unit: 2187

308-4908. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-9600.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 308-9051 (for formal communications intended for entry)

Or:

(703) 305-6606 (for informal or draft communications, please label "PROPOSED" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park 2,
2121 Crystal Drive,
Arlington, VA., Sixth Floor (Receptionist).

M. Namazi
June 30, 2002

Dohyun Yoo
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TECHNOLOGY CENTER 2100